

図1

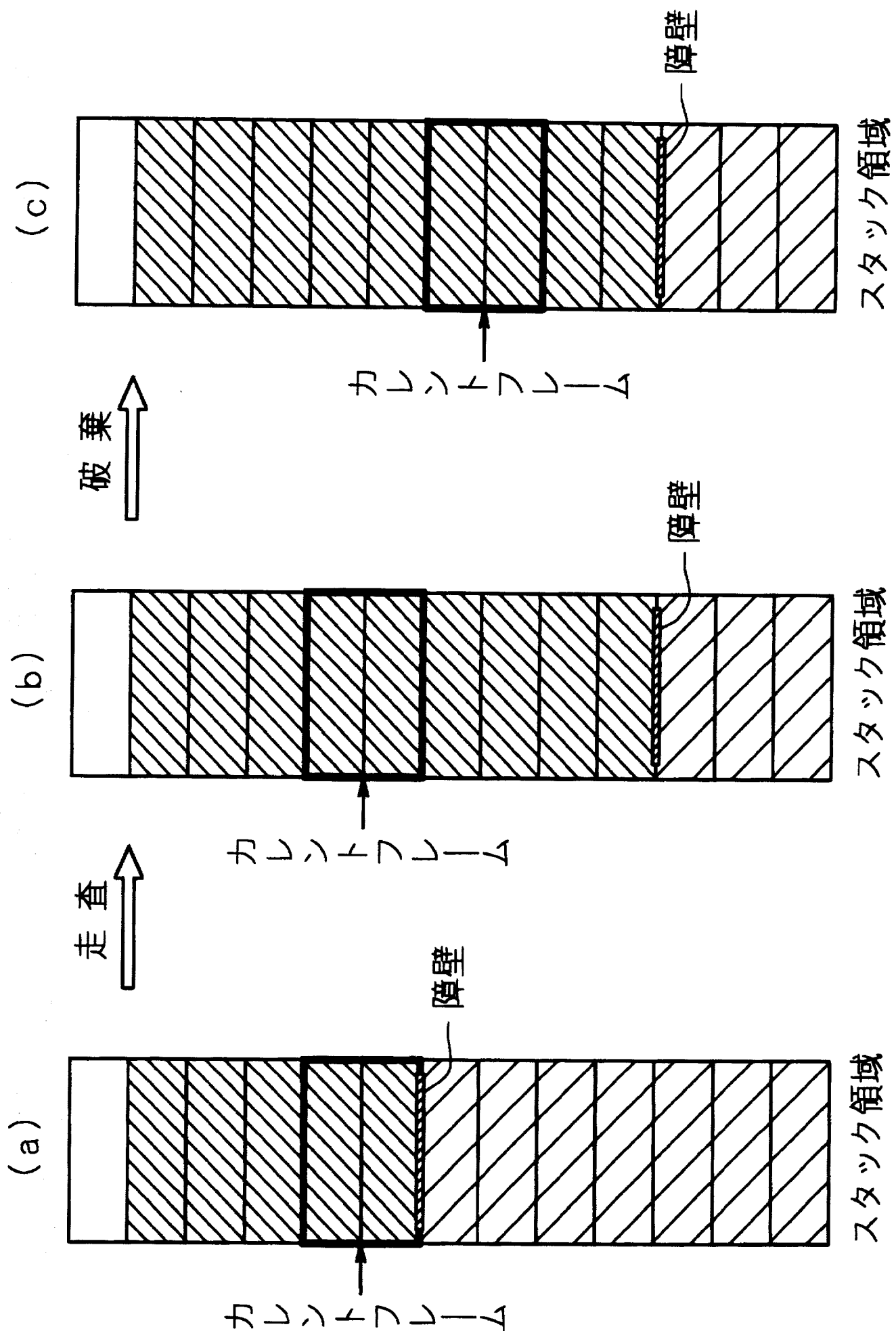
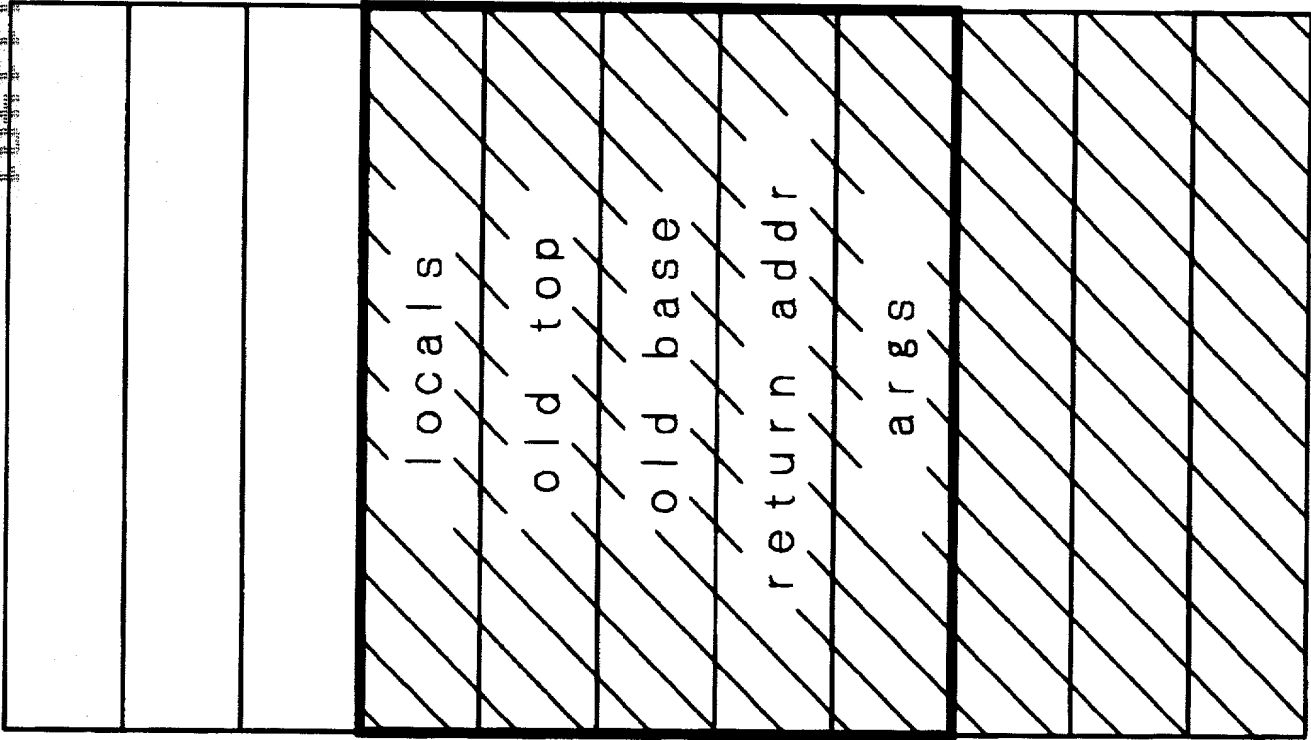


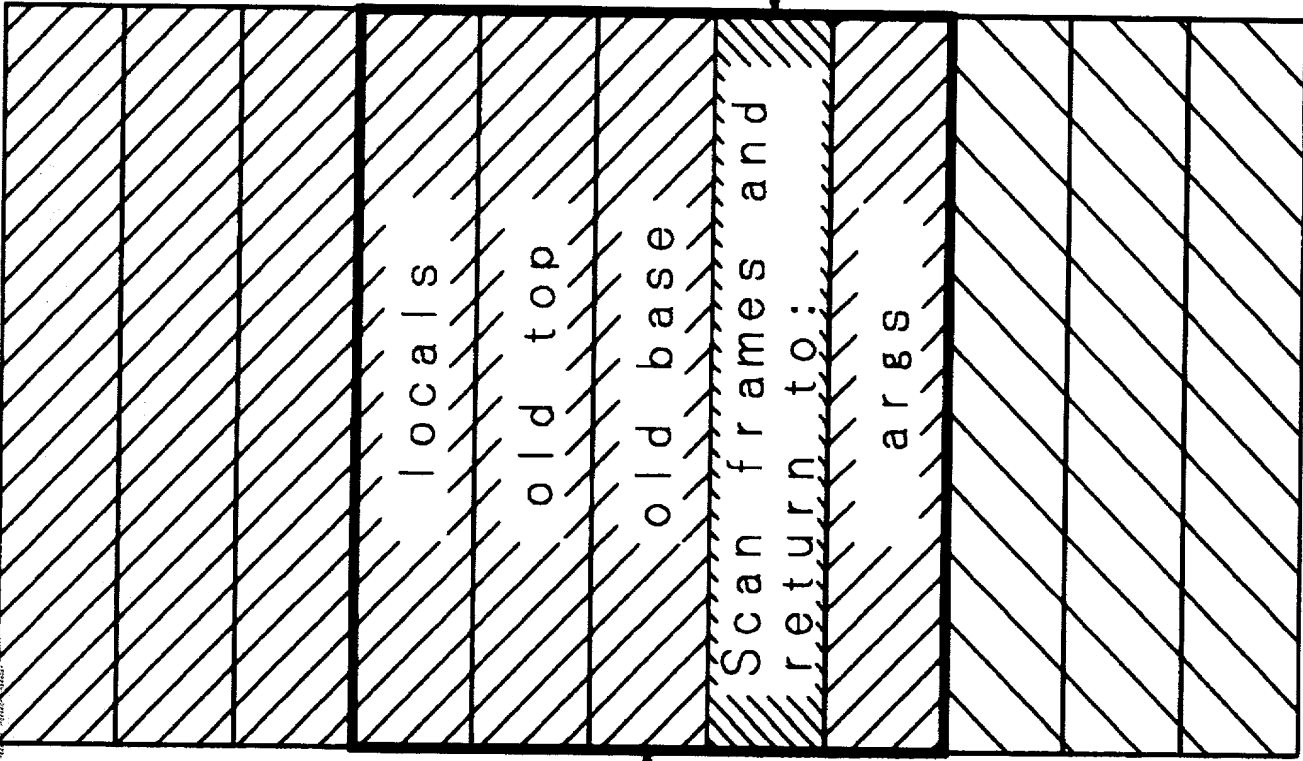
図 2

(a)



障壁設定前

(b)



障壁設定後

図3

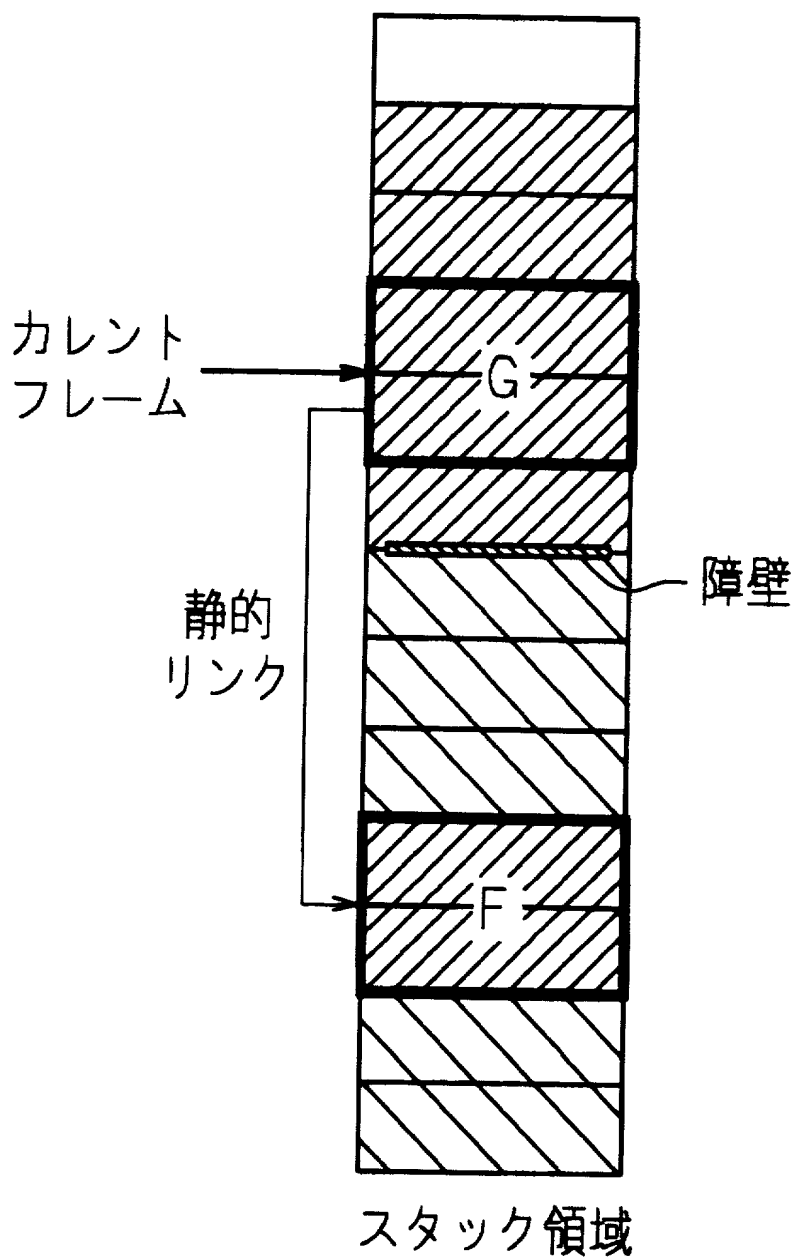


図 4

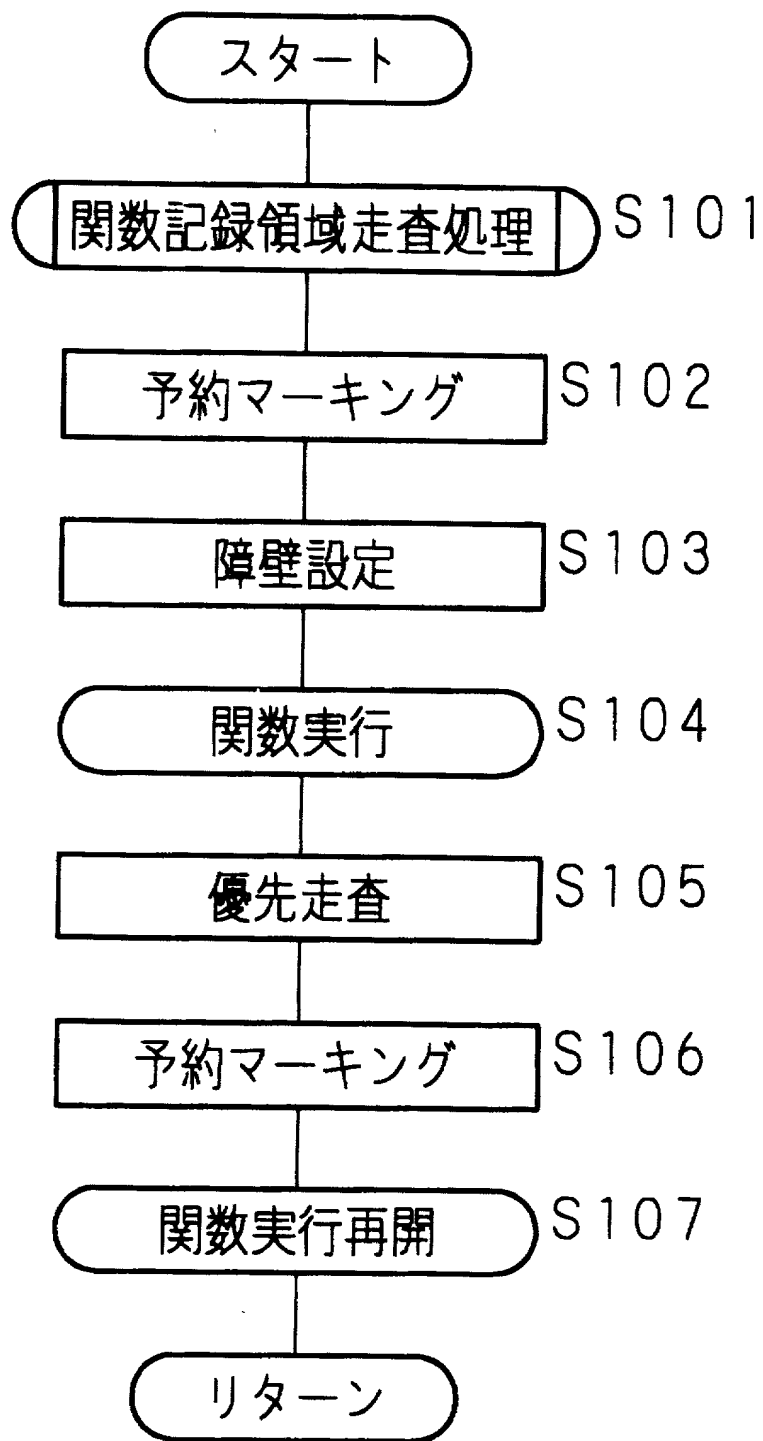


図5

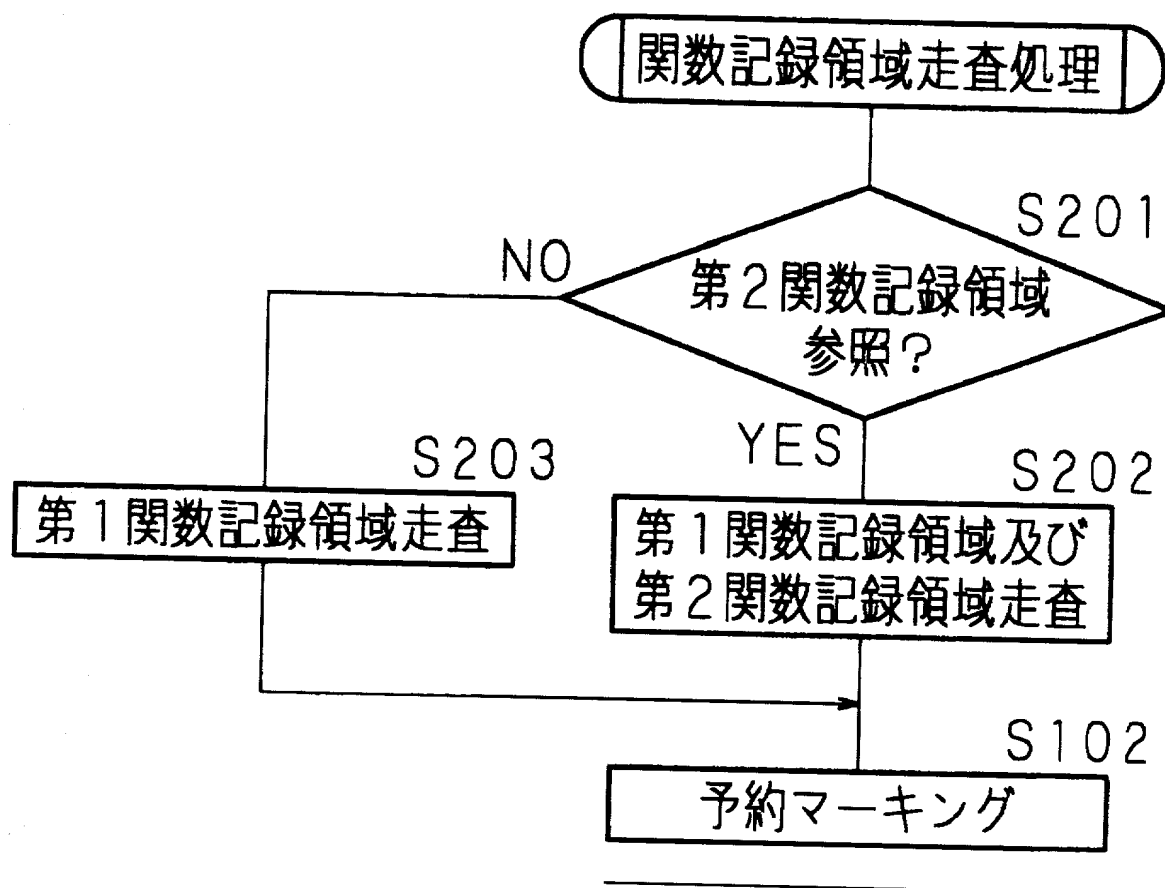


图 6

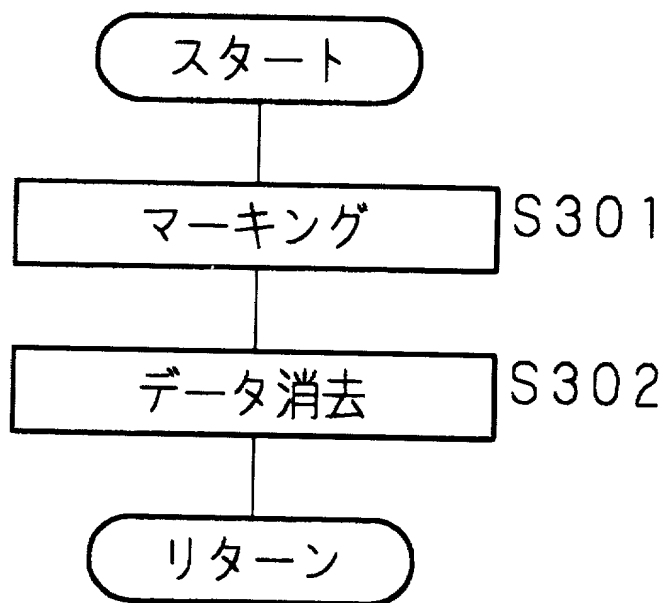
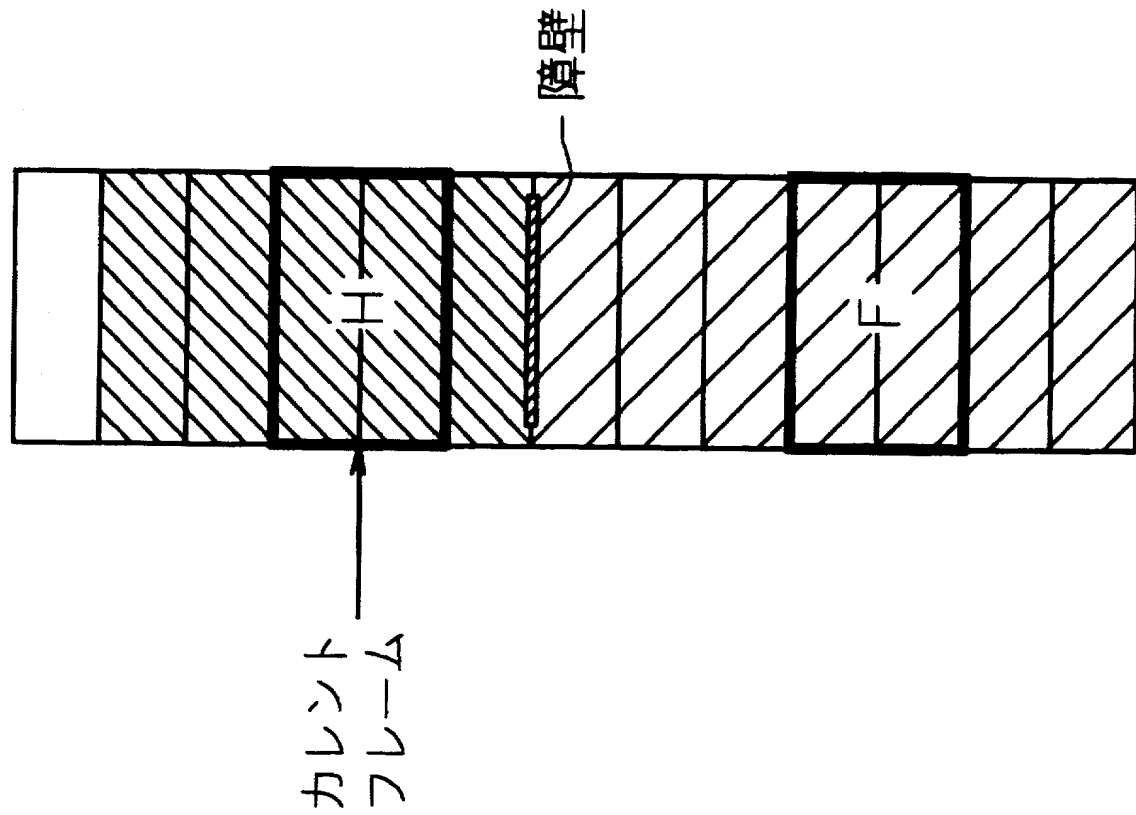


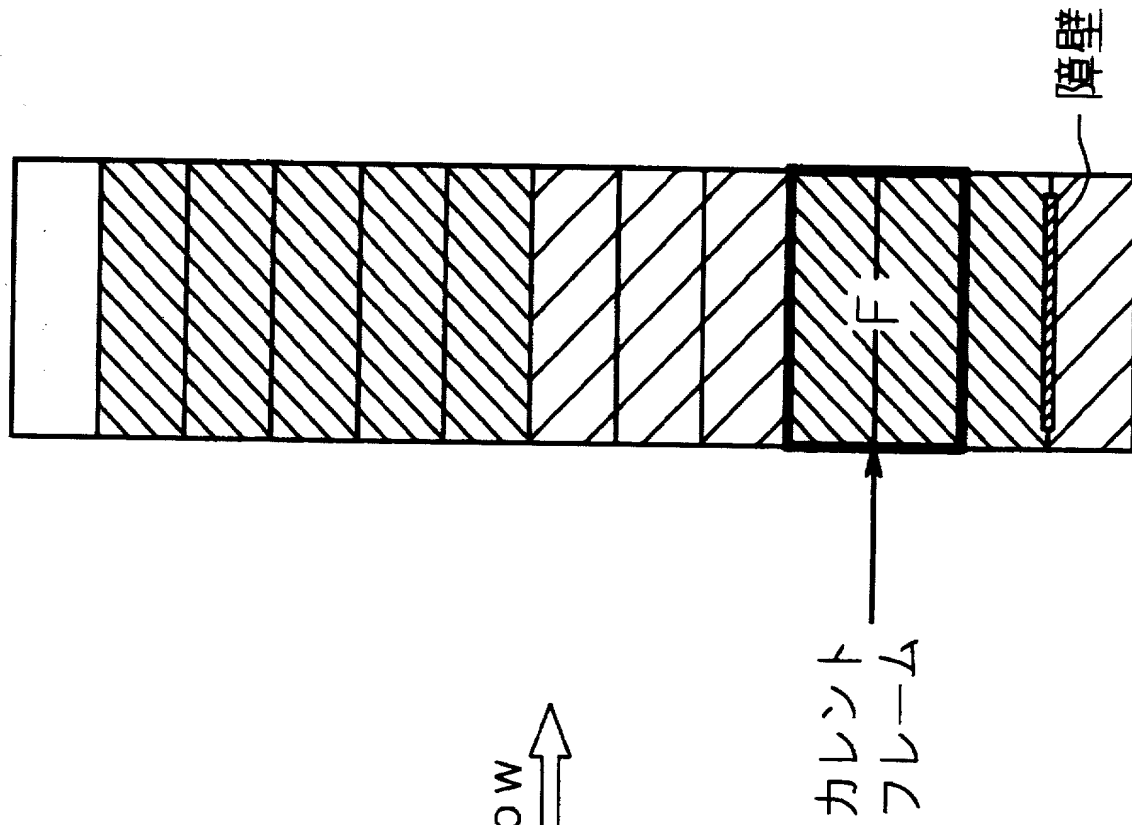
図 7

(a)



throw

(b)



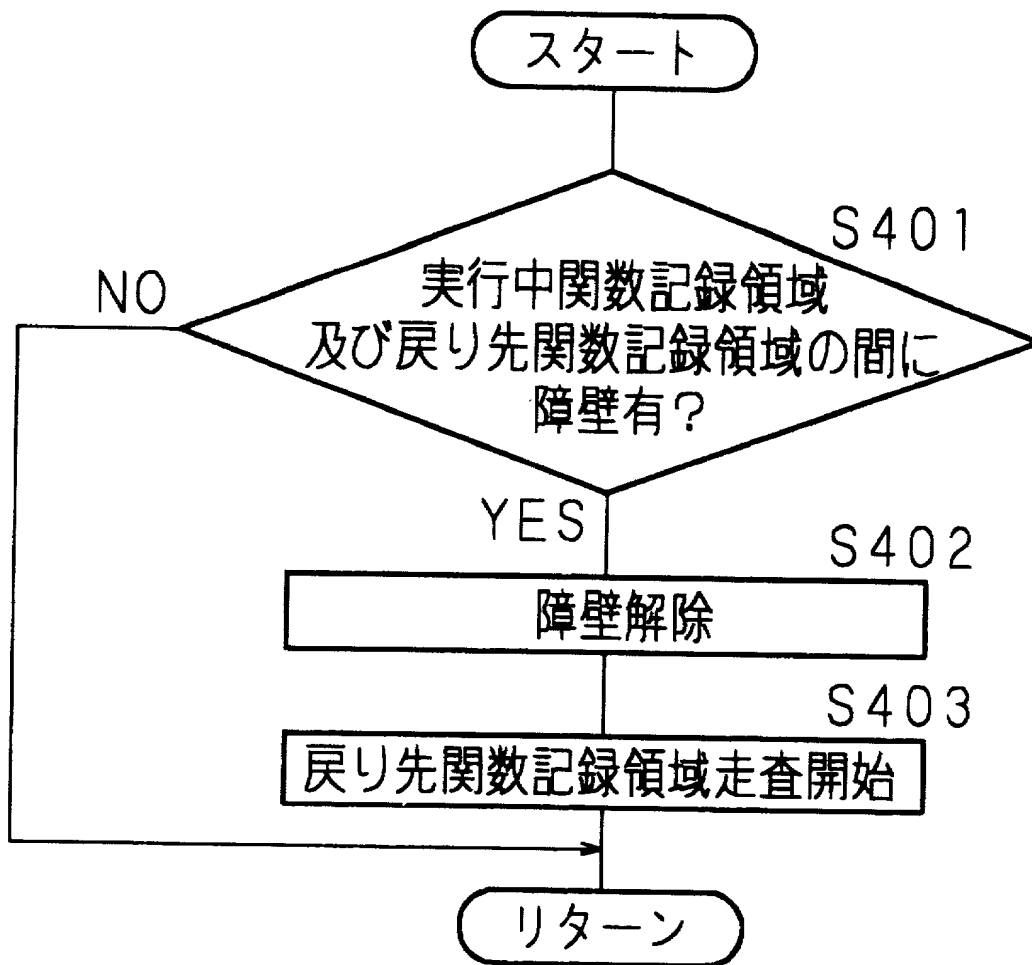
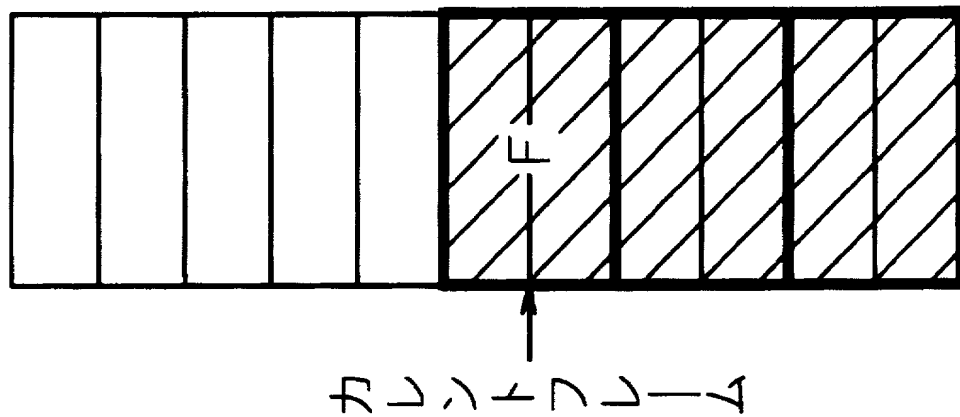
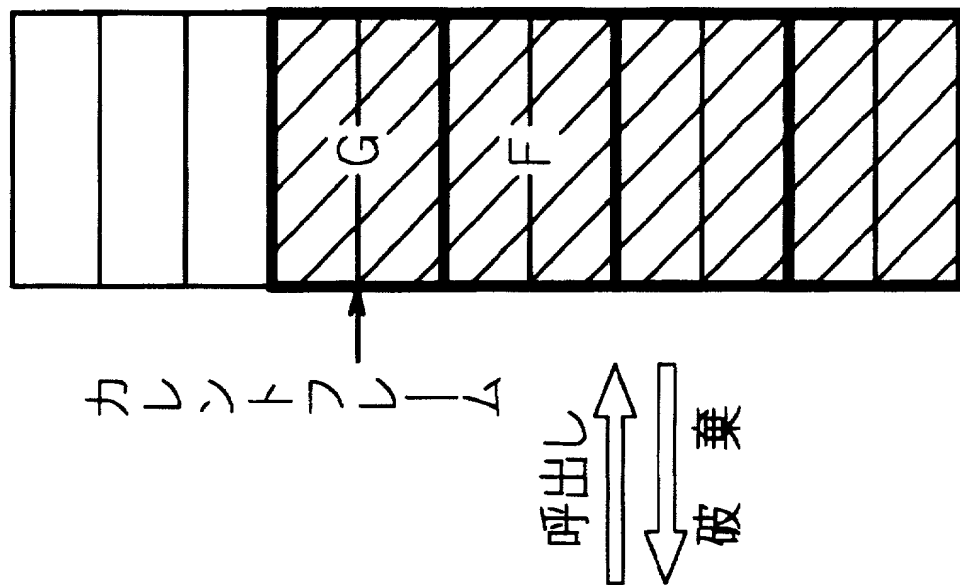


図 9

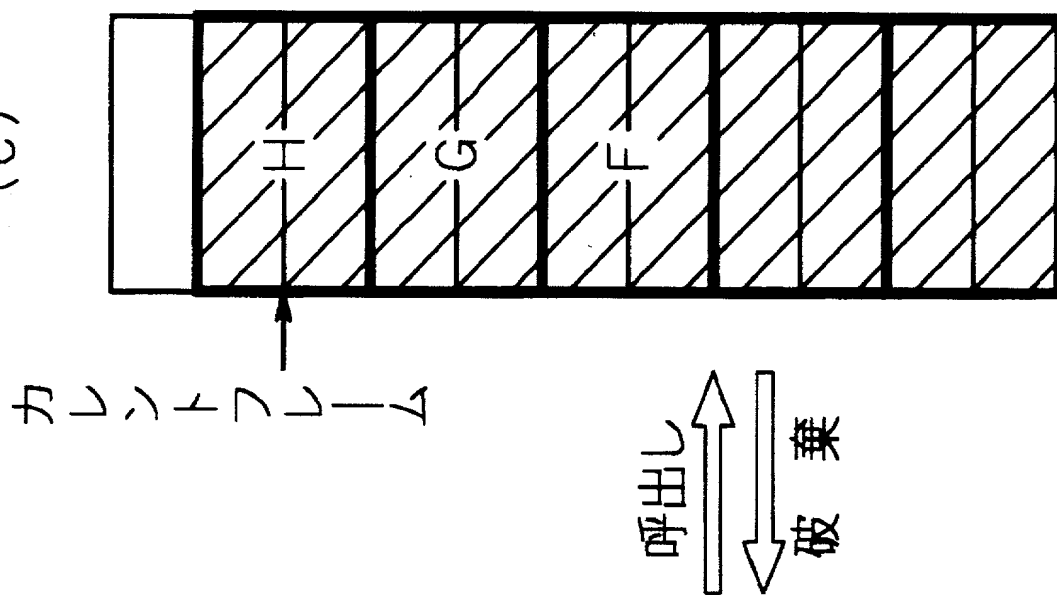
(a)



(b)



(c)



呼出し
破棄

図 10

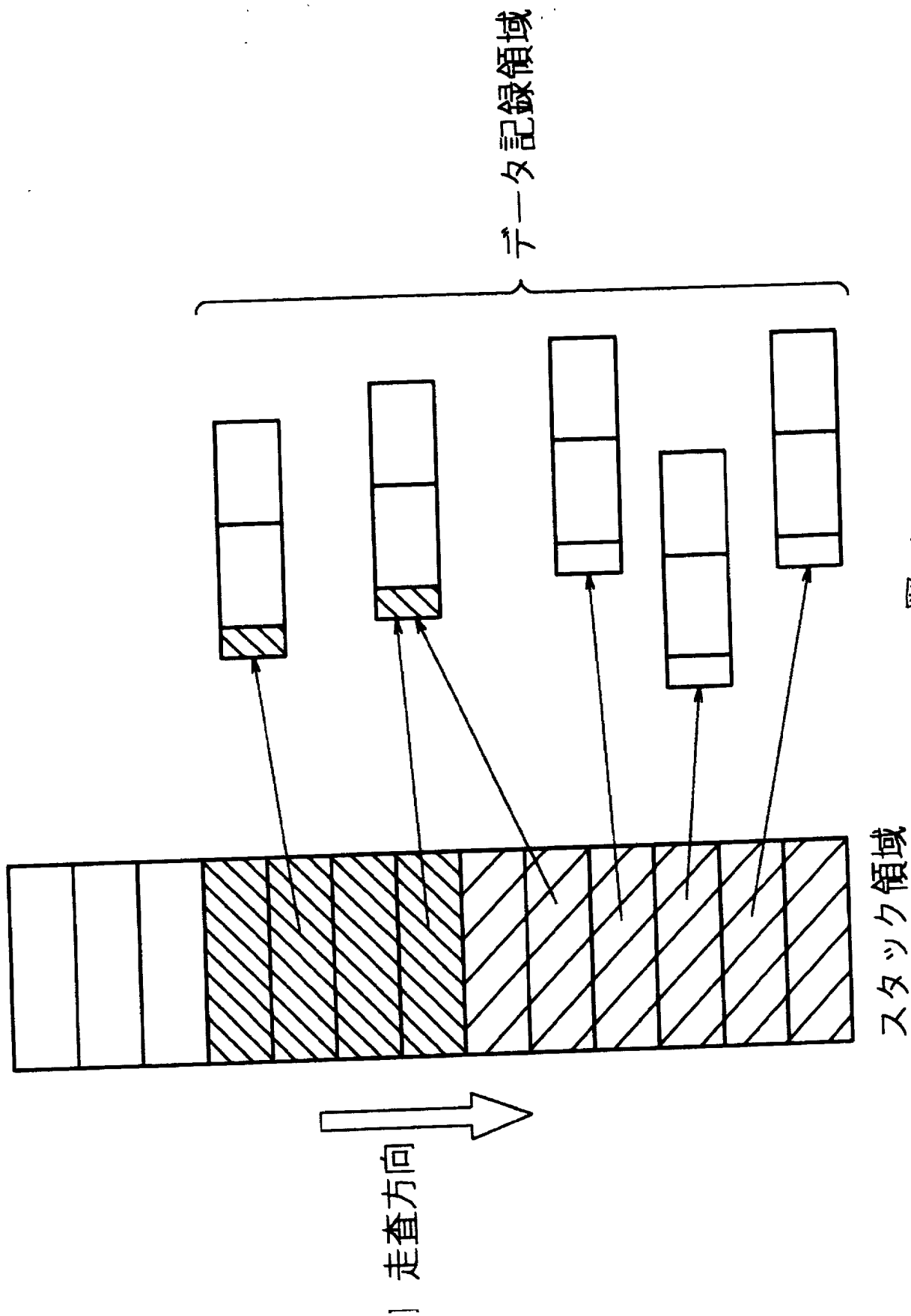
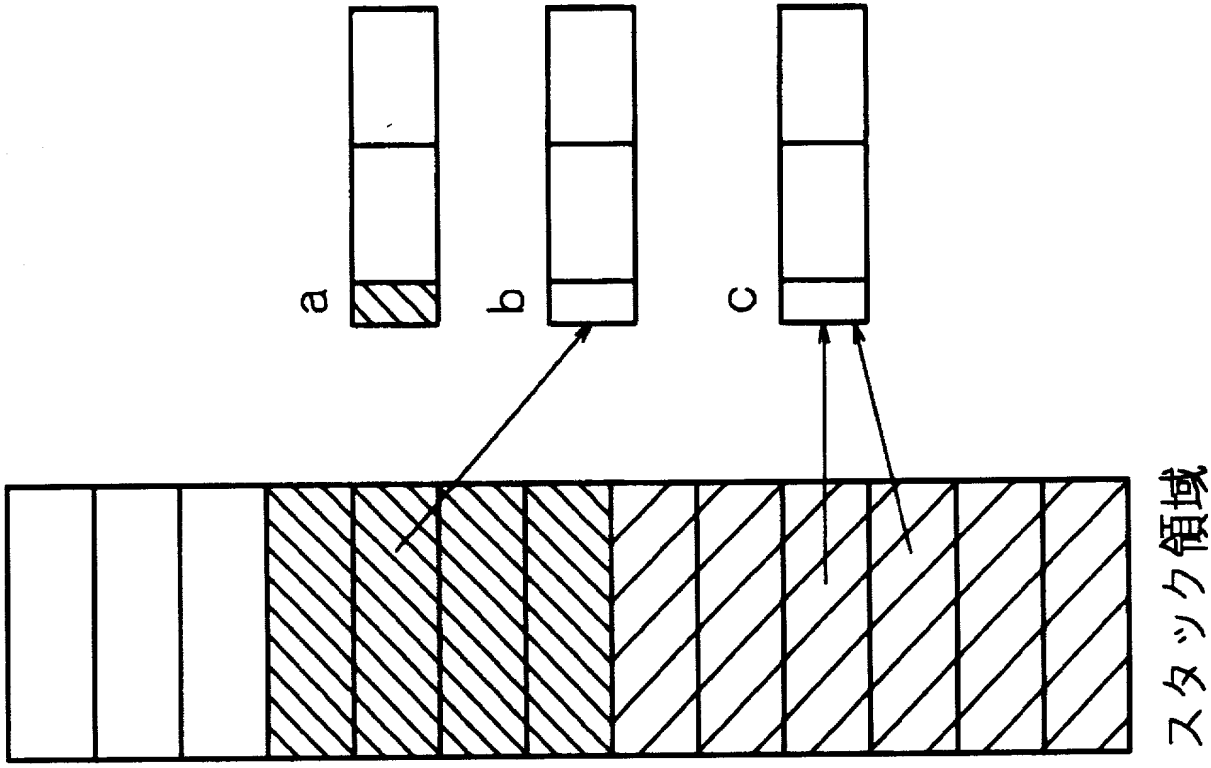
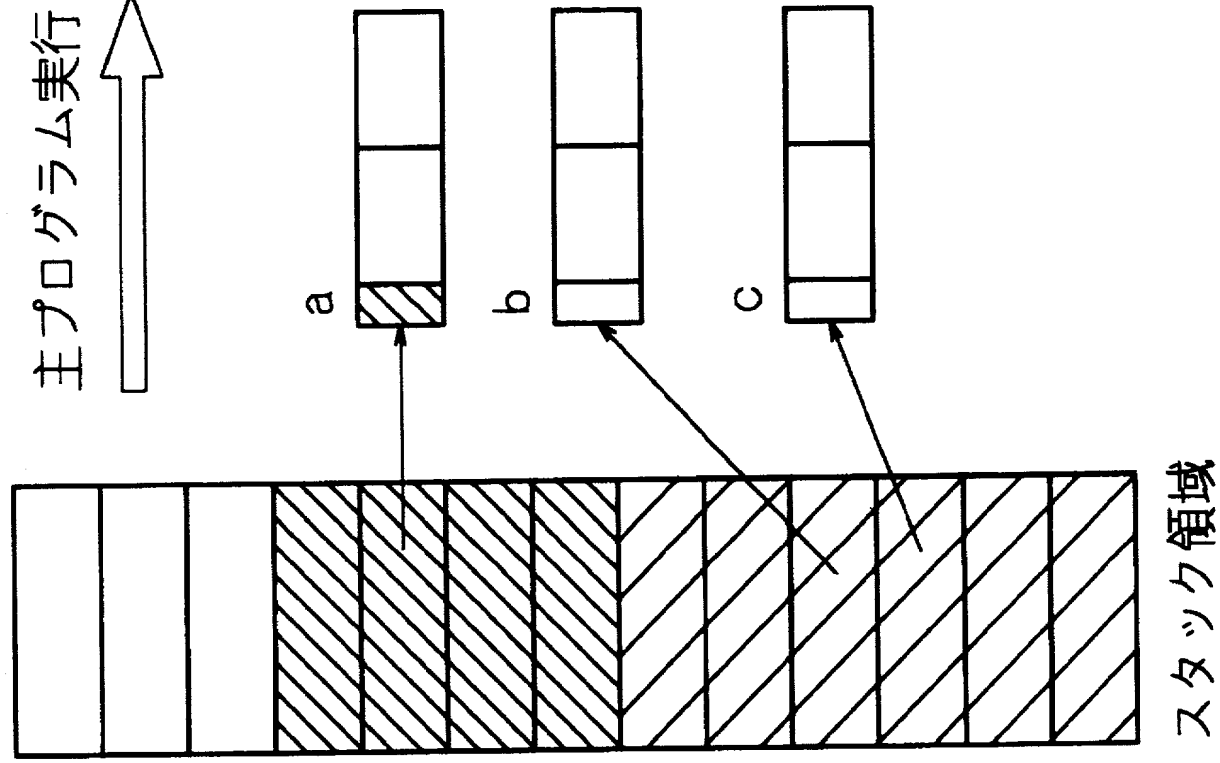


図11

Figure 20 (b)

主プログラム実行
↑

(a)



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(a)

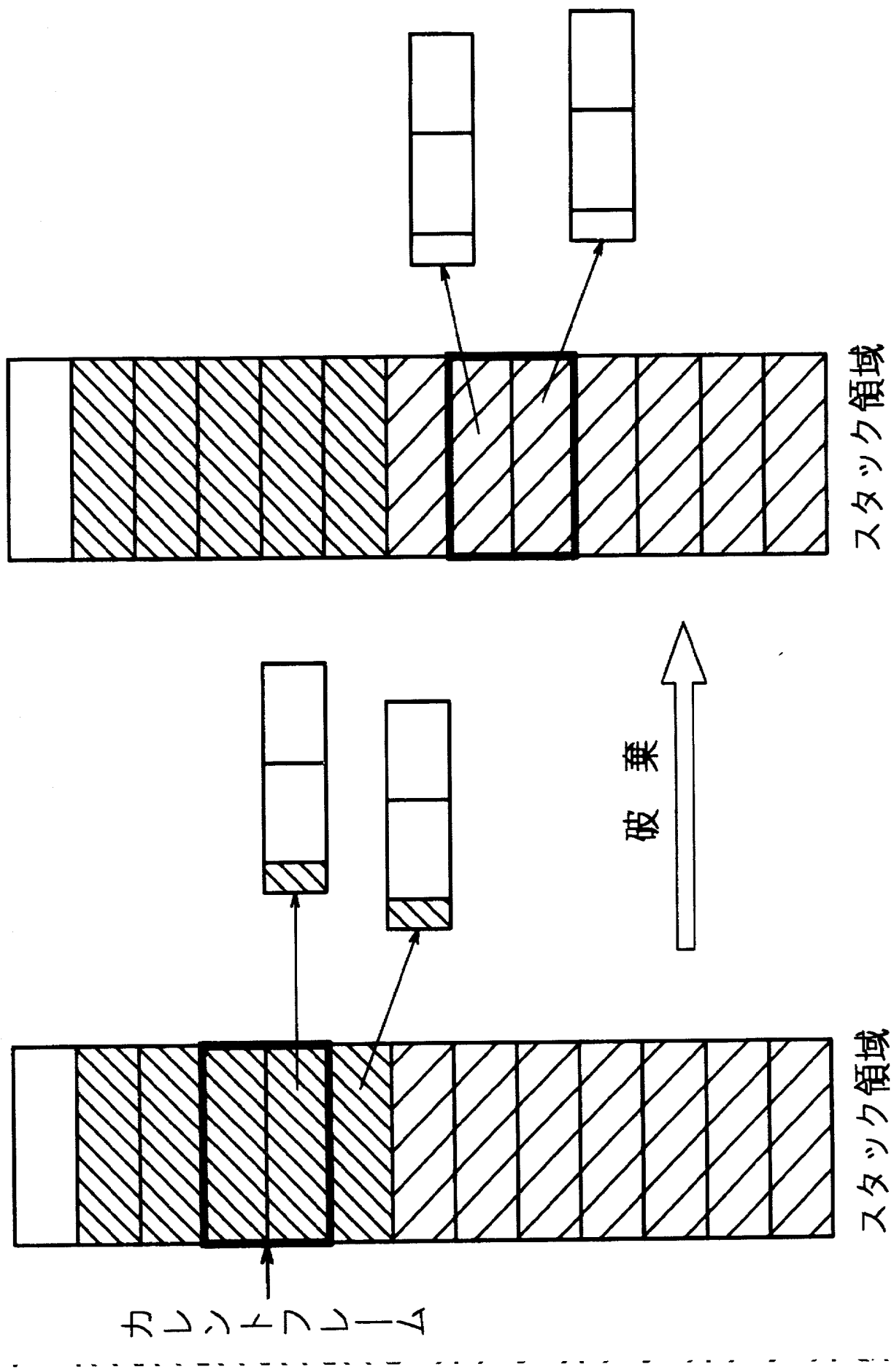


図13

The diagram illustrates a vertical stack of memory frames. The stack area is labeled 'スタック領域' at the bottom. It contains several frames, with two specific frames highlighted: frame 'G' (the current frame, labeled 'カレントフレーム') and frame 'F' (the static link, labeled '静的リンク'). Frame 'G' is shaded with diagonal lines and has a thick border. Frame 'F' is also shaded with diagonal lines and has a thick border. An arrow points from the text 'カレントフレーム' to frame 'G'. Another arrow points from the text '静的リンク' to frame 'F'. The stack area is divided into sections by horizontal lines, with some sections having diagonal hatching.

图 14

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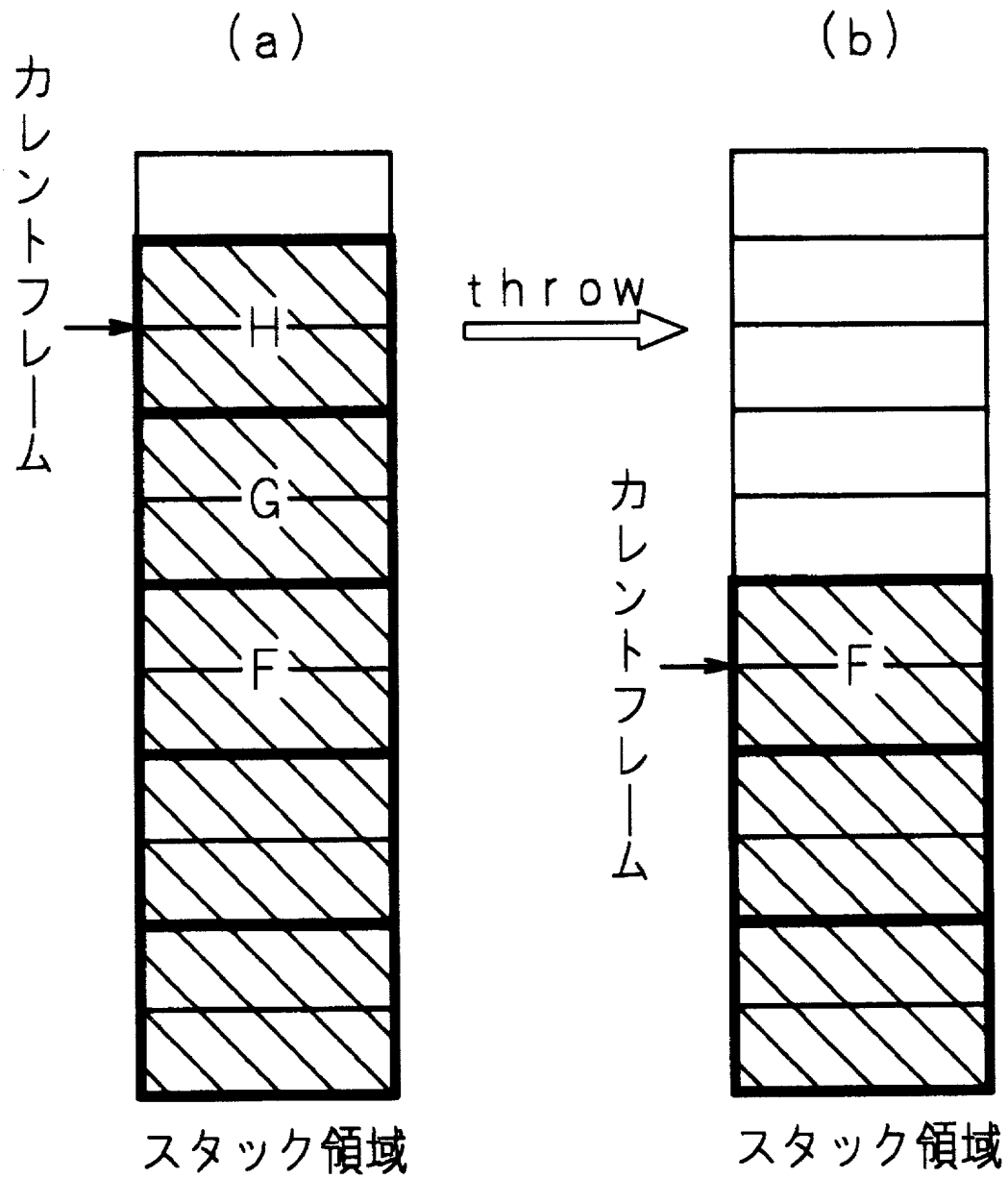


図 15